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12/27/01

MAILING LABEL NO. EL730605248US
DATE OF DEPOSIT: DECEMBER 27, 2001

J1011 U.S. PTO
 10/040868
 12/27/01

(SIGNATURE)

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Inventor Tae-Sub CHANG and Dong-Ho LEE:
For: HIGH FREQUENCY SEMICONDUCTOR CHIP PACKAGE AND
SUBSTRATE

Enclosures:

- | | | |
|-------------------------------------|---|--|
| <input checked="" type="checkbox"/> | Specification (pages 1-9); claims (pages 10-13); abstract (page 14) | |
| <input checked="" type="checkbox"/> | FIVE sheet(s) of FORMAL drawings | |
| <input checked="" type="checkbox"/> | Declaration or Combined Declaration and Power of Attorney | |
| <input checked="" type="checkbox"/> | Newly executed (original or copy) | |
| <input checked="" type="checkbox"/> | Assignment with cover sheet | |
| | Assignee Name and Address: | Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do, Korea |
| <input checked="" type="checkbox"/> | Certified copy of priority document | |
| <input checked="" type="checkbox"/> | Return Postcard | |

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PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.
Any deficiency or overpayment should be charged or credited to deposit account
number 13-1703. A duplicate copy of this sheet is enclosed.



20575

PATENT TRADEMARK OFFICE

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollom

Reg. No. 28,881

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PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

01/14/2002 SDIRETA1 00000250 10040868

01 FC:101	740.00 OP
02 FC:103	126.00 OP

PTO-1556
(5/87)